Design of low sidelobe Microstrip antenna array

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Abstract: A low side lobe level is obtained using Tschebyscheff amplitude tapering. To obtain amplitude tapering Wilkinson power divider is used. Two antenna structures are designed, first is conventional corporate feed array that operates at 5.5 GHz with a side lobe level of -12.7 dB. The second structure is designed through appropriate Tschebyscheff distribution. It results in a decrease in SLL to -15.4dB. The simulated results are presented and discussed.

Keywords: microstrip antenna array, sidelobe level, Tschebyscheff distribution, Wilkinson power divider

I. Introduction

Nowadays microstrip antenna arrays are used widely in the satellites, aircraft and other such applications where low- profile, light weight are main considerations. Antenna parameters such as sidelobe level (SLL) [1] and bandwidth, play a very crucial role in many radar systems. The sidelobe levels between -5 dB and -20 dB is referred as low sidelobe levels, and levels below -20 dB are referred as ultralow side lobe levels[2]. Printed antenna arrays with SLL lower than -25 dB are difficult to realize mainly due to: mutual coupling between radiating elements, surface wave effect, parasitic radiation from a feeding network, and tolerances in fabrication [1]. In the present work, a 4 element microstrip antenna array is designed using Tschebyscheff amplitude distribution[3] producing sidelobe level of -15.4 dB.

II. Antenna Array Design

The process starts with the designing of an inset fed rectangular patch antenna on a glass epoxy substrate having relative permittivity $\varepsilon_r = 4.36$ and thickness of 1.6 mm. The array is designed by arranging these four microstrip line feed patch antennas in linear configuration. The array is fed by corporate feed network. The matching technique used is quarter wave transformer impedance matching. The inter element spacing is an important factor in design of antenna array, so it has been varied and the effect of this variation on radiation pattern is observed. The variation of spacing changes the sidelobe level and beamwidth. The optimal results are obtained at a distance of $0.75\lambda_0$. The conventional 4 element microstrip antenna array is shown in fig. 1.

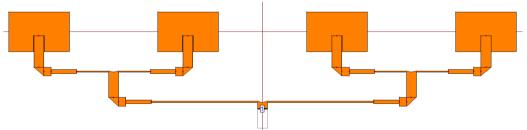


Figure 1. Conventional 4 – Element Microstrip Antenna Array

Fig.2 shows the modified antenna array to get low sidelobe level. As can be observed this new design includes Wilkinson power divider[4]. The unequal Wilkinson power divider is responsible for the creation of the required tapered amplitude distribution. This power divider has a split ratio of 2:1. The array is designed for an appropriate Tschebyscheff distribution of -16 dB taking into account the implementation restrictions of high impedance lines needed in the design of unequal Wilkinson power dividers. The normalized amplitude of each antenna element is shown in Table 1.

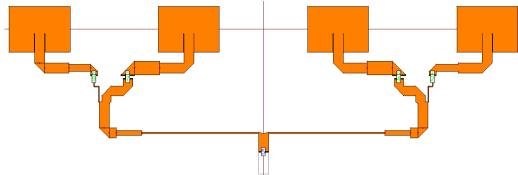


Figure. 2 Modified Antenna Array

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1×4 Array Amplitude	Element Number			
Distributions	1	2	3	4
Normalized Amplitude	0.707	1	1	0.707

2.1 Theoretical analysis of Rectangular patch

The parameters of rectangular microstrip antenna such as width, effective dielectric constant, effective length, length extension and acutal length are calculated using the given equations [5]:

$$w = \frac{c}{2f_0\sqrt{(1+\varepsilon_r)/2}}$$
(1)

$$\varepsilon_{reff} = \frac{\varepsilon_r + 1}{2} + \frac{\varepsilon_r - 1}{2\sqrt{1+\frac{12h}{2}}}$$
(2)

$$l_{eff} = \frac{c}{2f_0\sqrt{\varepsilon_{reff}}}$$
(3)

$$\Delta l = 0.412h \frac{(\varepsilon_{reff} + 0.3).(\frac{w}{h} + 0.264)}{(\varepsilon_{reff} + 0.3).(\frac{w}{h} + 0.264)}$$
(4)

$$\left(\varepsilon_{reff} - 0.258\right) \cdot \left(\frac{w}{h} + 0.8\right)$$

$$l = l_{eff} - 2\Delta l \tag{5}$$

In which, l =length of the rectangular patch

w = width of the rectangular patch

h= thickness of the substrate material

 $f_0 = resonant frequency$

The width of microstrip transmission line (W_f) is calculated using equation (6):

$$Zc = \begin{cases} \frac{60}{\sqrt{\varepsilon_{reff}}} \ln \left[\frac{8h}{W_f} + \frac{W_f}{4h} \right], & \frac{W_f}{h} \le 1\\ \frac{120\pi}{\sqrt{\varepsilon_{reff}} \left[\frac{W_f}{h} + 1.393 + 0.667 \ln \left[\frac{W_f}{h} + 1.444 \right] \right]}, & \frac{W_f}{h} > 1 \end{cases}$$
(6)

2.2 Dimensions of Antennas

The dimension of antennas e.g.length, width, dielectric constant, height, resonant frequency etc. has been calculated using above formulas which has been tabulated below.

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Parameters	Values		
Resonant frequency, f_o	5.5 GHz		
Dielectric constant, ε_r	4.36		
Substrate thickness, h	1.6 mm		
Length of patch, L	12.5 mm		
Width of patch, W	16.7 mm		
Width of 50 Ω transmission line, W_f	2.718 mm		
Inset depth, y_o	5 mm		
Notch width , g	0.091 mm		

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Table 2	Antenna	Dimensions
1 4010 2.	7 mitemiu	Dimensions

III. RESULT AND DISCUSSION

3.1 Return Loss Plot

The S_{11} (input reflection coefficient) plot for the given patch antenna array is drawn by the simulation using IE3D software. The s11 plot for the conventional antenna array and modified array is shown in fig. 3(a) and 3(b) respectively. The conventional array is giving maximum return loss of -26 dB at 5.5 GHz and modified array is giving a return loss of -21.45 dB.

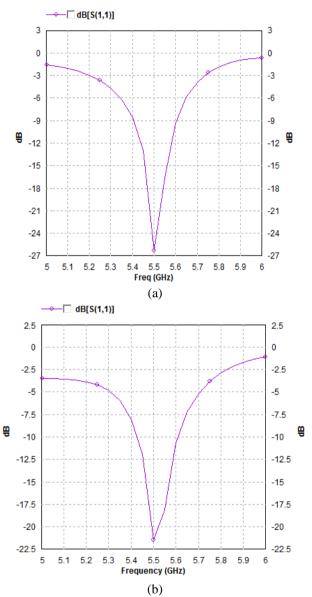


Figure 3(a) Return loss plot for conventional antenna array (b) Return loss plot for modified antenna array

3.2 Radiation Pattern

The radiation pattern of conventional microstrip antenna array is shown in fig 4(a). it gives the maximum gain of 10.1673 dB in broadside direction and side lobe level of -12.7 dB. The modified array gives a side lobe level of -15.4 dB with maximum gain of 10.57522 dB in broadside direction as shown in fig. 4(b). The half power beamwidth of conventional array and modified array is 16° and 20.1° respectively. Both array radiates in broadside direction with no back lobes.

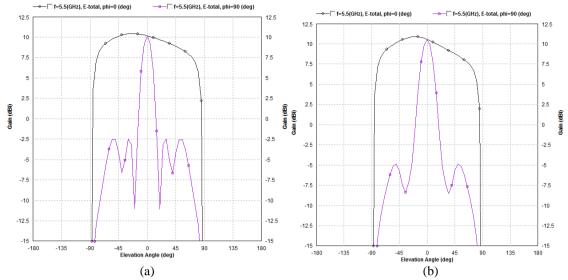


Figure 4(a) Radiation Pattern for conventional antenna array (b) Radiation Pattern for modified antenna array

IV. Conclusion

Two arrays are designed at 5.5 GHz. Tschebyscheff amplitude tapering is used to decrease the side lobe level. The results show that side lobe level is reduced by -2.7 dB. The simulated result shows that as side lobe level decreases beam width increases. The side lobe level can be further reduced if a substrate having low dielectric constant is used. The antenna shall be suitable for wireless communication system applications in C band (4-8 GHz).

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